

# EPV Technologies

## Newsletter



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This message contains news related to EPV products produced and distributed by EPV Technologies.

The EPV products suite answer problems such as **Managing Performance, Tuning and Capacity Planning** on the most common platforms, **allowing huge savings on HW and SW costs.**

Greater details and information on EPV products and solutions can be found at <http://www.epvtech.com> or writing to [epv.info@epvtech.com](mailto:epv.info@epvtech.com).

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#### 1) Tech Papers

##### **Capture Ratio using Address Space interval records (SMF30)**

##### **Why over 100% values can be considered as normal**

Fabio Massimo Ottaviani – EPV Technologies

*In mainframe environments the capture ratio concept is universally known. Performance Analysts normally use an index (CR72 in the following) that is the ratio between the total CPU attributed to all the service classes (collected in the SMF 72 records) and the total CPU used (collected in the SMF 70 records) as a measure of the system overhead.*

*The system overhead is the amount of CPU used for activities (such as paging, swapping, part of the I/O operation, etc.) not directly correlated to a specific address space, transaction or enclave. The impact of these activities is not the same on all the workload types: a TSO is continuously swapped in and out while CICS and IMS regions are normally non swappable, some batch jobs do a huge amount of I/Os compared to their CPU usage while other applications work mostly in memory.*

*So an important characteristic of the capture ratio is its workload dependence.*

*A widely accepted ROT (Rule Of Thumb) is to consider as normal values between 85% and 95%.*

*If you want to receive the white paper you can reply to this e-mail writing **"Capture Ratio using Address Space interval records"** in the subject.*

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## **2) Tech Support**

### **Wrong values in SMF 72 I/O times**

The problem has been solved in z/OS 1.6 and 1.7.

At the moment IBM doesn't foresee a correction for z/OS 1.5 (where the problem has been detected).

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## **3) Tech News**

### **CMG-Italia Seminar - "WLM 2006: errors, half errors and unknowns"**

The seminar will be held in Milan on March 29th 2006.

The speaker will be Fabio Massimo Ottaviani from EPV Technologies.

More details in the next newsletter or on the CMG-Italia web site at: <http://www.cmgitalia.it>

If you are interested in participating but your company is not associated to CMG Italia contact us at [epv.info@epvtech.com](mailto:epv.info@epvtech.com).

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