z10 Capacity Planning Issues
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IBM z10 machines present innovative architecture and features (HiperDispatch) designed to exploit the speed of the new 4.4 GHz processors. LSPR benchmarks have also been redesigned to estimate z10 capacity and to update the software configuration to the most current release.
These two elements introduce a number of Capacity Planning issues you have to be aware of before trying to estimate z10 capacity compared to old machines.
After a short introduction to the z10 design and to HiperDispatch, this paper discusses these issues. With the up to date information and advice provided you will be able to perform more reliable capacity planning studies.