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<p>30 July 2008 - Number 7</p>	<p>Past numbers of this newsletter are available on the web at http://www.epvtech.com</p>

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1) Tech Papers	HiperDispatch Logical Processors and Weight Management - Part 1
Abstract	Fabio Massimo Ottaviani - EPV Technologies

In the last few years, the power and number of the physical processors available on mainframe hardware has greatly increased, allowing the concentration of a much higher number of LPARs than before on a single machine.

A side effect of this high number of LPARs is an increase of the number of defined logical processors compared to the number of physical CPs.

These factors tend to reduce the probability for a logical processor to be re-dispatched to the same physical processor and therefore reuse instructions and data previously loaded in the Level 1 cache (the amount of cache memory dedicated to each processor).

A L1 cache miss will cause data and instructions to be loaded from the Level 2 cache (the cache memory shared among all the physical processors packaged in a book). Performance degradation and overhead occurs when this happens because the access to L2 cache will require more CPU cycles to be performed. However if the logical processor has been dispatched to a different physical processor which belongs to a different book, the required instructions and data have to be loaded from the previously used L2 cache and performance degradation and overhead can be much worse.

HiperDispatch has been designed to minimise the number of L1 cache misses and, if a L1 cache miss occurs, to maximise the probability of finding instructions and data in the L2 cache of the book where the logical processor is dispatched.

To reach this goal, a new weight called polarization weight has been introduced; polarization weight is a key element in the HiperDispatch design because it is the way z/OS uses to give PR/SM indications on how logical processors should be dispatched to the physical processors. This paper, using real life examples, will discuss:

PR/SM and IRD logical processors and weight management (Part 1);

HiperDispatch logical processors and weight management (Part 2).

If you want to receive the paper you can reply to this e-mail writing "HiperDispatch Logical Processors and Weight Management" in the subject.

2) Tech News

Upcoming events

CMGAE & EuroCMG 2008, Wien, 9-10 October

CMGAE 2008 will be held in Wien on October 9-10. The conference will host the 11th edition of EuroCMG.

EPV Technologies will present the paper: "Bigger Savings Using New z Technologies".

More details at: <http://www.cmg-ae.at/>

UKCMG Free Forum, London, 14 October

The new UKCMG Free Forum will take place on the 14th October 2008 at the CBI Conference Centre, Centrepont, London, WC1 1DU.

This one-day conference will keep you abreast of developments and in touch with key IT people. There will be a multi-tracked agenda and a full table top exhibition running alongside the conference allowing you the opportunity to network with other key IT people.

Inspired Solutions, official distributor of EPV products in UK and Ireland, and EPV Technologies will sponsor the conference.

More details at: <http://www.ukcmg.org.uk/ifOct2008.html>

EPV User Group 2008, Rome, 21 October

The 6th EPV User Group will be held in Rome, at the Residenza di Ripetta, on October 21st 2008.

The EPV User Group is a "not to miss" event for all Performance Analysts; it will give you the opportunity to share ideas with qualified experts and to listen to some of the EPV customers experiences.

Preliminary Agenda (draft)

09:30 Registration

10:00 Welcome and introduction, EPV Technologies

10:15 EPV for z/OS experience, User presentation

10:45 What's new in EPV for z/OS 8.0, EPV Technologies

11:15 Coffee break

11:45 EPV for Unix and Windows experience, User presentation

12:15 EPV Parser for SMF user experience, User presentation

12:45 What's new in EPV for DB2 3.0, EPV Technologies

13:15 Lunch

14:30 Do you really want to save money on the mainframe ?, EPV Technologies

16:00 User Group end

Mark the day in your agenda to avoid missing this event.

GSE UK, Kenilworth, 28-29 October

The GSE UK Conference 2008 will be held in Kenilworth on 28th-29th October.

GSE is the UK's premiere independent annual event addressing the challenges of delivering & maintaining solutions based on IBM architecture & open standards.

EPV Technologies will present the papers:

- "Estimating GCP, zAAP and zIIP Latent Demand"
- "Managing Websphere-DB2 transactions with WLM"

More details at: <http://www.gse.org.uk/>

3) Tech Notes

IBM WSC White Paper

Migrating SMF from Data Set Recording to Log Stream Logging

Beginning with z/OS V1R9, the System Management Facility (SMF) can be configured to use System Logger to write records to log streams. This paper gives an overview of the benefits of migrating SMF to log streams, reviews performance considerations and will describe a migration process using the IBM Poughkeepsie z/OS Integration Test environment as an example.

More details at: <http://www-03.ibm.com/support/techdocs/atsmastr.nsf/WebIndex/WP101271>

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